Integrated Device Technology, Inc.
6024 Silver Creek Valley Road, San Jose, CA 95138

PRODUCT/PROCESS CHANGE NOTICE (PCN)

PCN #: **Q1512-01** DATE: **January 11, 2016**	MEANS OF DISTINGUISHING CHANGED DEVICES:
Product Affected: F1100NBGI(8)	☐ Product Mark ☐ Back Mark ☐ Date Code ■ Other Shipment after PCN effective date.
Date Effective: **April 11, 2016**	

Contact: IDT PCN DESK	Attachment: ■ Yes ☐ No
E-mail: pcndesk@idt.com	Samples: Contact your local sales representative for sample and datasheet requests.

DESCRIPTION AND PURPOSE OF CHANGE:

☐ Die Technology
☐ Wafer Fabrication Process
☐ Assembly Process
☐ Equipment
☐ Material
☐ Testing
☐ Manufacturing Site
■ Data Sheet
☐ Other - Die revision

This notification is to advise our customers that IDT has updated the data sheet of the above mentioned products. The purpose is to correct Pin 25 and its description.

Refer Attachment I for additional details.

This is an update of the datasheet only, no change of the silicon design, die revision, circuitry, process or device performance.

RELIABILITY/QUALIFICATION SUMMARY:
There is no expected change to the product quality or reliability performance.

CUSTOMER ACKNOWLEDGMENT OF RECEIPT:
IDT records indicate that you require written notification of this change. Please use the acknowledgement below or E-Mail to grant approval or request additional information. If IDT does not receive acknowledgement within 30 days of this notice it will be assumed that this change is acceptable.
IDT reserves the right to ship either version manufactured after the process change effective date until the inventory on the earlier version has been depleted.

Customer: _____	☐*Approval for shipments prior to effective date.*
Name/Date: _____	E-Mail Address: _____
Title: _____	Phone # /Fax #: _____

CUSTOMER COMMENTS: _____

IDT ACKNOWLEDGMENT OF RECEIPT:

RECD. BY: _____   DATE: _____

IDT

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PRODUCT/PROCESS CHANGE NOTICE (PCN)

ATTACHMENT I - PCN #: Q1512-01

PCN Type: Data Sheet Change

Data Sheet Change: Yes

Detail of Change:

This notification is to advise our customers that IDT has updated the data sheet of the above mentioned products. The purpose is to correct Pin 25 and its description. Besides, there are few updates made:

1. Pin 2, 8, 20 Name and Pin 4, 6, 12, 15, 23, 26, 31, 34 Name to keep the pin name consistent.
2. Added typ-op plots for low current (LC) mode operation.
3. On EVKIT PICTURE, modified annotation associated with headers JP2 and JP3.
4. Added components and their values to the BOM for low current (LC) mode.

Refer to the below table for changes.

This is an update of the datasheet only, no change of the silicon design, die revision, circuitry, process or device performance.

Description	Change From	Change To
Pin 25 Name & Function	NC. Make Certain this pin is not connected. It is normally reserved for selecting low current mode which the F1100 does not offer	LC_MODE. Low_Current Mode. Set this pin to low or ground for LC mode. Set to high or No-Connect for Standard mode. There is an internal pull-up resistor.
Pin 2, 8, 20 Name	RF_Artn, RF_Brtn, LO_rtn	RFA_rtn, RFB_rtn, LO_rtn
Pin 4, 6, 12, 15, 23, 26, 31, 34 Name	N.C.	NC